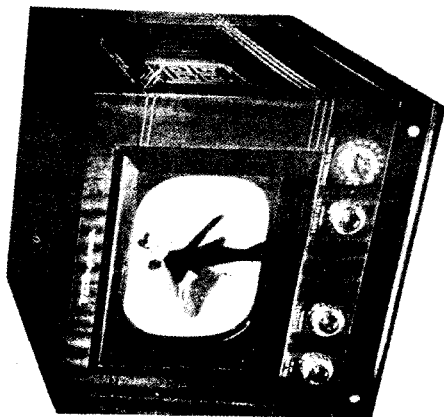


MODEL H-223, Ch. V-2150-01



MODEL H - 223

SPECIFICATIONS

FREQUENCY RANGES:

Channel Number	Channel Frequency (mc.)	Video Carrier Frequency (mc.)	Audio Carrier Frequency (mc.)	Receiver H-F Oscillator Frequency (mc.)
1	not used	--	--	--
2	54 - 60	55.25	59.75	81.35
3	50 - 66	61.25	65.75	87.35
4	56 - 72	67.25	71.75	93.35
5	76 - 82	77.25	81.75	103.35
6	82 - 88	83.25	87.75	109.35
7	174 - 180	175.25	179.75	201.35
8	180 - 186	181.25	185.75	207.35
9	186 - 192	187.25	191.75	213.35
10	192 - 198	193.25	197.75	219.35
11	198 - 204	199.25	203.75	225.35
12	204 - 210	205.25	209.75	231.35
13	210 - 216	211.25	215.75	237.35

FINE TUNING RANGE:

..... Plus or minus 400 kc. on channel 2;
..... Plus or minus 2 mc. on channel 13.

OPERATING VOLTAGE:

..... 105 to 120 volts, 60 cycles

POWER CONSUMPTION: 200 watts

AUDIO POWER OUTPUT:

Undistorted 1.2 watts
Maximum 1.5 watts

RECEIVER ANTENNA INPUT IMPEDANCE:

..... 300 ohms balanced or 72 ohms unbalanced

LOUDSPEAKER:

Type 5 $\frac{1}{4}$ " P. N.
Voice Coil Impedance 3.2 ohms at 400 cycles

VIDEO CARRIER INTERMEDIATE FREQUENCY: 26.1 mc.

TUBE COMPLEMENT:

VIDEO RESPONSE: 3.5 mc.

AUDIO CARRIER INTERMEDIATE FREQUENCY

..... 4.5 mc.

AUDIO DISCRIMINATOR BAND WIDTH

(between peaks) 150 kc.

FOCUS: Magnetic

SWEEP DEFLECTION: Magnetic

SCANNING: Interlaced 525 line.

HORIZONTAL SCANNING FREQUENCY:

..... 15,750 CPS

VERTICAL SCANNING FREQUENCY: 60 CPS

FRAME FREQUENCY (picture repetition rate):

..... 30 CPS

HIGH VOLTAGE WARNING

The danger accompanying shock is always present when the receiver is operated outside the cabinet or when the rear cover is removed from the cabinet. Only a person familiar with the precautions to be observed when working with high-voltage equipment should service this receiver.

CATHODE RAY TUBE HANDLING PRECAUTIONS

Shatterproof goggles and heavy gloves should be worn at all times when handling the cathode ray tube. The tube should not be handled in the vicinity of any person not so equipped. When handling the cathode ray tube, always keep it away from the body.

The cathode ray tube bulb, due to its large surface area and high vacuum contained within, is subjected to high air pressure. More than ordinary care is required to prevent shattering the tube. The large end of the bulb, particularly the rim of the viewing surface, must not be struck, scratched, or subjected to more than moderate pressure at any time. If the tube sticks or fails to slip smoothly into place during installation, remove the tube and determine the cause of the trouble - - DO NOT FORCE THE TUBE

CIRCUIT DESCRIPTION -- V-2150-01 CHASSIS

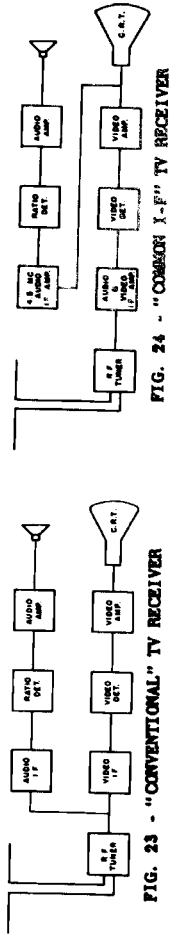


FIG. 23 - "CONVENTIONAL" TV RECEIVER

The following circuit description is based on the assumption that the reader is familiar with standard television principles and practices. The particular circuits described are those included in the Westinghouse Chassis No. V-2150-01. For general television information the reader is referred to one of the several television textbooks on the market.

The function of each circuit component is given in the parts list, and specific components will be mentioned only when necessary for purposes of clarification.

GENERAL.

The V-2150-01 chassis utilizes the "common I-F" TV system. As shown in Figs. 23 and 24, this system differs somewhat from the so-called "conventional" system.

In the conventional type TV receiver, the video and audio R-F carriers are converted into their respective I-F frequencies in the mixer stage of the R-F tuner. They are then separated, usually at the plate of the mixer or the first I-F stage, and the audio and video components are amplified and detected in separate channels. Every effort is made to keep the audio component from appearing on the grid of the picture tube. This is usually accomplished by the use of traps in the video I-F system and, in some cases, the video amplifier.

With the common I-F system, the audio and video carriers are converted to their respective I-F frequencies in the same manner as in the conventional system. However, the two I-F signals are not separated into different I-F channels at this point. Instead, they are amplified in a common I-F amplifier, and both signals appear at the video detector. Since the audio and video carriers are originally transmitted 4.5 mc. apart as specified in F.C.C. standards and this separation is accurately maintained by temperature-controlled crystal oscillators in the transmitter, the two I-F signals appearing at the video detector are separated by exactly 4.5 mc. In addition to the function for which it is named, the video detector serves as a mixer for the two I-F signals. The mixing of the audio I-F carrier

picture is not squared in the picture mask, loosen the wing nut and move it to the left or right so as to rotate the deflection yoke. The picture will tilt to the left or right with the deflection yoke rotation.

FOCUS CONTROL

The focus control (Fig. 2) should be adjusted with the brightness and contrast controls in their normal positions. If correct focusing cannot be obtained, refer above to "FOCUS COIL".

HEIGHT AND VERTICAL LINEARITY

The height adjustment controls the overall height of the picture, while the vertical linearity adjustment governs contraction or expansion of the upper portion only. For this reason, a balance between the two controls is necessary to make the picture symmetrical and fill the mask vertically.

WIDTH

The width adjustment controls the width of the picture. It should be adjusted so the picture fills the mask horizontally. No horizontal linearity adjustment is required.

HIGH VOLTAGE OSCILLATOR

The setting of the high voltage oscillator trimmer (C506) determines the voltage applied to the second anode of the C.R.T. To correctly adjust the trimmer:

1. Turn off the receiver and disconnect the high voltage lead from the C.R.T.
2. Connect 9 megohms of resistance between the high voltage lead and the chassis. The 9 megohms of resistance should consist of a 9 one-megohm, one-watt resistors connected in series.
3. Connect a kilovoltmeter across the 9 megohm resistance, and make certain that all connections are well insulated.
4. Turn on the receiver and adjust C506 for maximum voltage indication on the meter.

HORIZONTAL RINGING COIL

To adjust the horizontal ringing coil, tune in a station, set the horizontal hold control at approximately the middle of its range and adjust the ringing coil (L403) until the picture is properly "locked-in".

CIRCUIT DESCRIPTION

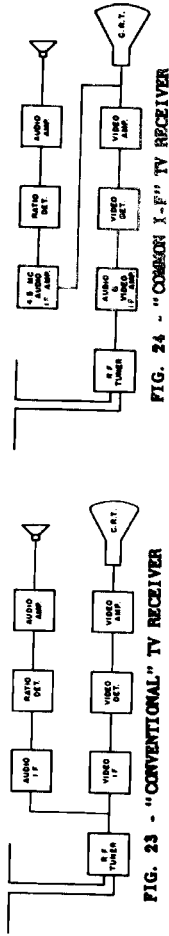


FIG. 2A. "MAYNARD Y-F" TV PROCESSING

er with the video I-F carrier produces a 4.5 mc. beat signal that is frequency modulated in accordance with the audio I-F carrier.

The manner in which the 4.5 mc. beat signal is obtained can be likened to the mixer action in the well-known superhet receiver. The incoming video I-F signal serves in lieu of a local oscillator and beats against the incoming audio I-F signal to produce the sum and difference frequencies at the output of the video detector. Since the sum frequency will fall far outside the pass-band of the video amplifier, it can be disregarded. This leaves only the difference frequency of 4.5 mc. to be considered.

After passing through the video amplifier, the 4.5 mc. signal is fed to a 4.5 mc. audio I-F amplifier, which amplifies the signal sufficiently to drive the ratio detector. An audio amplifier raises the output of the ratio detector to a level suitable for exciting the speaker.

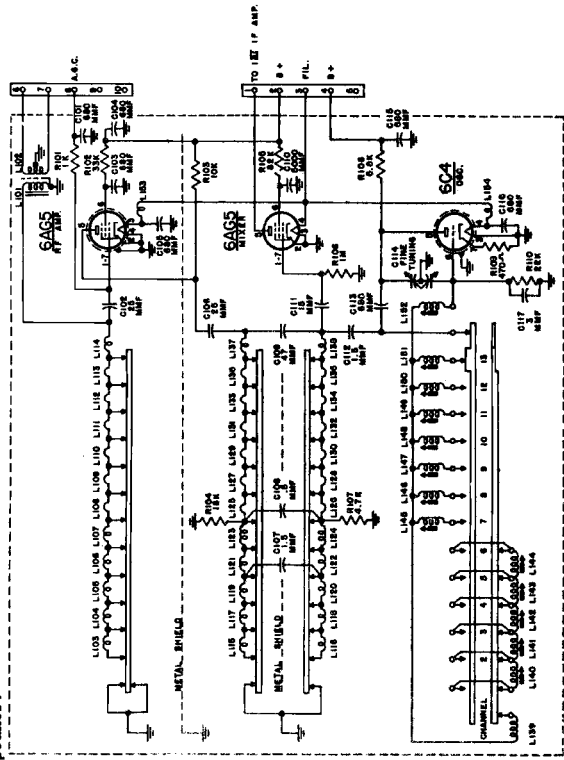


FIG. 25 - R-F TUNER

The R-F amplifier grid and plate circuit, along with the mixer grid circuit, are tuned by incremental inductances. As the channel selector switch is rotated toward the higher frequency channel positions, the inductances are progressively shorted and the circuits are resonated to the desired frequency.

The oscillator tuning circuit consists of separate inductances that are switched in and out of the circuit as the channel selector switch is rotated. As the name implies, the fine tuning control (C114) provides a fine adjustment of the oscillator frequency and makes it possible to obtain correct tuning despite slight frequency drifts, changes in tube characteristics due to aging, etc.

A 6C4 tube operating in a modified Colpitts circuit serves as the H-F oscillator. Its output is injected into the mixer grid circuit through C112. Rectification of the oscillator injection voltage in the mixer grid circuit provides part of the bias for the mixer tube, and the remainder of the bias is supplied by contact potential.

AGC voltage is applied to the grid of the R-F amplifier tube. This voltage controls the gain of the tube and keeps the picture contrast relatively constant under variations in signal strength. In addition, it tends to prevent blocking on very strong signals.

I-F AMPLIFIER (Fig. 26)

The V-2150-01 chassis uses a stagger-tuned, impedance-coupled I-F system. Stagger-tuning provides the required pass-band without resorting to complicated over-coupled transformers or "M-derived" and other types of filters. The plate load impedances are peaking coils, each of which is tuned to a different frequency. Interstage coupling is obtained through capacitors in a manner similar to that used in resistance-coupled audio amplifiers. Since there are only four tuned circuits in the system---and no traps---alignment is relatively simple as compared to the "conventional" system. AGC voltage is applied to the first two stages in order to maintain relatively constant video and audio output under varying signal strengths.

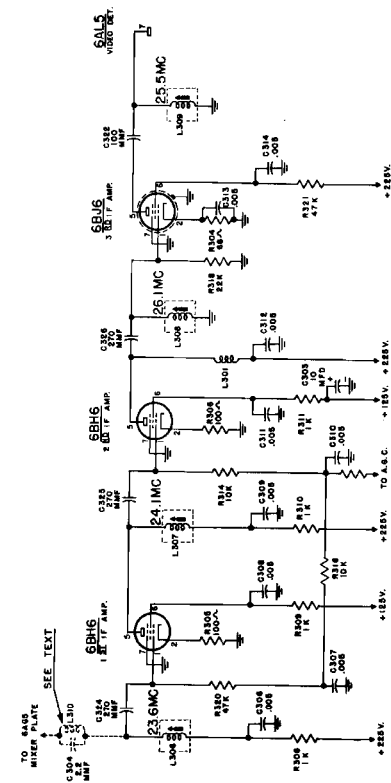
In early production of the V-2150-01 chassis, a trap (L310 and C304) was inserted between the mixer plate and the grid of the first I-F amplifier. This trap was provided to prevent the H-F oscillator voltage from "leaking through" to the grid of the I-F tube. In later production, however, the same result was accomplished by changing the length and dress of the mixer plate lead, and the trap was deleted. All stages of the I-F amplifier have LC isolation networks in their heater circuits. The function of these traps is to prevent interaction between stages and coupling of the I-F voltages into other stages

of the receiver through the heater--cathode capacitance of the tubes and the common heater line.

The I-F amplifier is designed so that the video I-F carrier appears at the 50% (6db) point on the I-F response curve, while the audio I-F carrier appears at a point 26 db below the video I-F carrier or at the 2.5% point. As a result, the amplitude of the audio I-F carrier is much less than that of the video I-F carrier at the video detector. This condition is essential for two reasons: First, the audio I-F signal must be of very low amplitude at the grid of the C.R.T. in order to prevent picture distortion; and second, the audio I-F amplitude must be low in comparison with the video I-F amplitude in order that the characteristics of the 4.5 mc. beat response of the two signals will be determined chiefly by the audio I-F signals and fluctuations in the video I-F signal will have less effect upon the 4.5 mc. beat response.

VIDEO DETECTOR AND AGC (Fig. 27)

The video detector is a diode type similar to those used in AM receivers. The I-F signal is applied to one plate of the 6AL5 twin-diode tube. On the positive portions of the video I-F signal, the diode will conduct and current will flow up through the contrast control to the cathode of the diode. This current will develop a voltage across the contrast control, and the voltage will vary with the modulation on the signal. C305 eliminates the higher-frequency I-F component by by-passing it to ground. The setting of the contrast control determines the amount of the signal that is applied to the grid of the video amplifier which, in turn, determines the voltage amplitude applied to the C.R.T. grid.



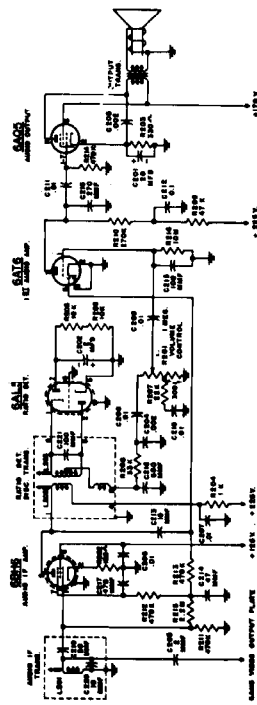


FIG. 29 - AUDIO CHANNEL

DC RESTORER, PHASE INVERTER AND SYNC SEP.

ARMOR (Fig. 30)

One half of a 12AU7 dual triode tube serves as a DC restorer and sync separator, while the other half serves as a phase inverter.

In one triode section, the cathode (pin 3) and the grid (pin 2) serve as a diode which performs the DC restorer function. The output of the video amplifier is applied to the cathode through C319 and R312. When the signal drives the cathode negative with respect to ground, the diode conducts and effectively shorts out R446. This shortens the time constant of the circuit that would otherwise include R446 as well as C319 and R312. As a result of the relatively short time constant, C319 will charge to almost the peak amplitude of the sync pulse which is "negative-going" at this point in the circuit. During the time that the applied signal is at zero or positive potential, the diode is non-conductive and the time constant as determined by C319, R312 and R446 is relatively long. During this time, C319 discharges slowly through R312 and R446 and develops a potential that is positive with respect to ground across the resistors. Since the charging rate of C319 is much greater than the discharge rate, the positive potential will be maintained throughout the cycle and its amplitude will be determined by the average amplitudes of the sync pulses. The positive voltage thus developed is applied as bias to the grid of the C.R.T. where it counteracts some of the positive voltage applied to the cathode of the C.R.T. through the voltage divider, R315, R322 and the brightness control. The varying bias applied to the grid then holds the blanking pedestals of the signal at a constant level, and the brightness control is adjusted so that this level corresponds to the cut-off point of the C.R.T. In this way, the DC component of the original signal is effectively re-inserted.

AUDIO CHANNEL (Fig. 29)

The 4.5 mc. signal is taken from the plate of the video amplifier and fed to the audio I-F amplifier. In this stage the signal is amplified sufficiently to drive the ratio detector. The function and operation of the audio channel is the same as that of an FM receiver except that the I-F is 4.5 mc. rather than 10.7 mc. The reader is referred to one of the several textbooks on the market for information regarding the operation of the ratio detector.

The AVC system used in the audio channel is unusual in that AVC is applied to the 6H6 suppressor grid as well as to the control grid. A portion of the audio I-F amplifier output is coupled through C213 to one of the diode plates in the 6AT6 first audio amplifier tube. The diode will conduct during the positive portion of the cycle, and current will flow through R213, R215, and R211 to ground. The current flow develops a voltage that is negative with respect to ground across these resistors. A portion of the developed voltage is applied to the suppressor grid, and a lesser portion is applied to the control grid due to the voltage divider action of the three resistors. Greater control over the stage gain is obtained by applying the AVC voltage to both grids rather than to the control grid only. The AVC line is by-passed for R-F by C214 which serves also as part of the time constant network.

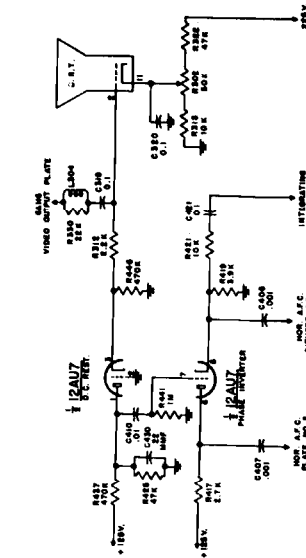


FIG. 30 - D-C RESTORER, PHASE INVERTER AND SYNC SEPARATOR

Synchronizing voltage for the vertical multivibrator is also taken from the cathode (pin 8) of the 12AU7 phase inverter section. From this point, the voltage is fed to the integrating network where it is re-shaped as explained below.

INTEGRATING NETWORK (Fig. 31)

The vertical sync pulses are coupled through C421 to the integrating network, the purpose of which is to develop the serrated pulse into a single pulse suitable for triggering the vertical multivibrator.

The input to the integrating network is effectively a voltage divider consisting of R421, R413, and the impedance of C421 which is approximately 30,000 ohms at 60 cycles. In addition to attenuating the sync pulses somewhat, the voltage divider attenuates any undesirable noise pulses and tends to make the circuit more immune to random noise.

Fig. 32 illustrates the integrating action of the first section of the integrating network. Since the other two cascaded sections function in the same manner as the first section, the discussion will be simplified by considering the first section only. As shown, the duration of each equalizing pulse is short as compared to the time between pulses. The time constant of the network is such that C423 will receive only a small charge during the time of an equalizing pulse, and in the interval between pulses C423 will discharge completely through R424 and R413. This results in the formation of a very low amplitude sawtooth voltage in the output.

At the same time as the grid and cathode of the triode section are functioning as a DC restorer, the entire triode section including the plate (pin 1) serves as a sync clipper. As a result of the DC restorer function described above, a potential that is approximately 25 volts positive with respect to ground is developed across R446. With this voltage applied to the cathode, the triode is operating well below cut-off. The circuit is actually a grounded-grid amplifier with the input signal applied to the cathode. The operating conditions are such that only the "negative-going" sync pulses are of sufficient amplitude to counteract the high bias and thus appear within the operating range of the tube. As a result, only a portion of each sync pulse appears at the plate of the tube; the blanking pedestals and video component are clipped because they appear below cut-off, and the tops of the sync pulses along with any strong noise peaks are clipped because the low plate voltage (approximately 8 volts) allows the tube to operate over only a very limited range. C430 serves to by-pass any video component that may "leak through".

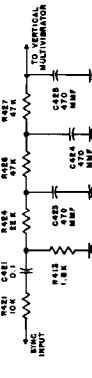


FIG. 31 - INTEGRATING NETWORK

The other triode section of the 12AU7 operates as a paraphase amplifier. The output from the sync clipper section of the 12AU7 is coupled into the grid of the phase inverter section through C410. The cathode (pin 8) is returned to ground through a 3900 ohm resistor. From across this resistance is taken one of the output voltages, and from across the plate load resistor is taken the other output voltage. These two voltages, which are 180 degrees out of phase and approximately equal in amplitude, are fed to the 6AL5 horizontal AFC tube where they are used to control the frequency of the horizontal multivibrator. The operation of the horizontal AFC circuit will be discussed later under "Horizontal AFC".

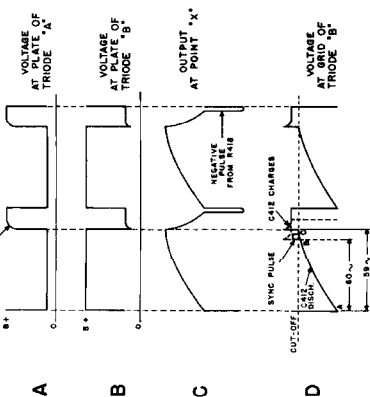


FIG. 34 - INSTANTANEOUS CONDITIONS IN MULTIVIBRATOR

curve as it would exist if the oscillator were "free-running" at a repetition rate of 59 C/P.S., while the dotted lines show the condition induced by the sync pulse. At point A on the curve, the grid is held well below cut off by the charge on C412. As the capacitor gradually discharges, the grid swings up toward cut off. If the oscillator were free-running, the tube would conduct suddenly at point C. Due to the introduction of the sync pulse, however, the condition is changed somewhat.

The sync pulse from the integrating network is applied to the grid of triode "A". This pulse, which is negative in polarity, appears at a time when triode "A" is conducting, and its effect will be to drive triode "A" into cut off. At the same time, the pulse will be inverted by the phase reversal characteristics of the tube and applied to the grid of triode "B" as a positive pulse. The positive pulse will drive triode "B" into conduction at point B instead of allowing the non-conduction period to extend to point C as would occur if the oscillator were free-running. It can be seen that the free-running frequency of the oscillator must be slightly lower than the sync frequency in order that the multivibrator will "lock-in" with the sync pulses. This condition is obtained when the vertical hold control (R404) is correctly adjusted.

VERTICAL DEFLECTION CIRCUIT (Fig. 35)

The action of the multivibrator was discussed in the preceding section. Referring to Fig. 34-B, it will be noted that the voltage at the plate of triode "B" in the multivibrator is a square wave. This voltage serves as a switch and controls the charge and discharge of C414 which is connected in series with R418 across the output of the tube. During the period when triode "B" is conducting, the tube is effectively a short circuit

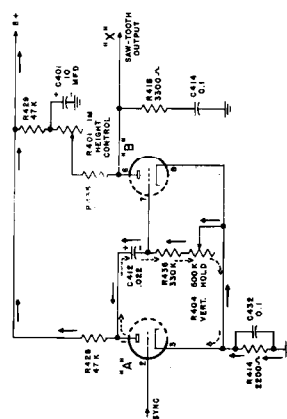


FIG. 33 - MULTIVIBRATOR ACTION

As the charge accumulates on C412, the charging current decreases. This reduces the voltage drop across R414 and R428, and the resultant increase in plate voltage and decrease in bias voltage allows triode "A" to conduct. When triode "A" conducts, its internal resistance is greatly decreased, and C412 will discharge through the path indicated by the dotted arrows. The discharge current flowing through R436 and R404 develops a potential that makes the grid of triode "B" negative with respect to the cathode, and triode "B" will be driven into cut-off.

The discharge of C412 will continue along an exponential curve, the negative potential applied to the grid of triode "B" slowly decreasing, until the point is reached where triode "B" will conduct again. When this occurs, the triode "B" cathode current added to the triode "A" cathode current will raise the voltage developed across R414 sufficiently to cut off triode "A". It should be noted that triode "B" will not be cut off at this time because its grid is returned to its cathode rather than to ground. When triode "A" cuts off, our original starting conditions are obtained, and the cycle will be repeated.

It can be seen that the free-running repetition rate of the oscillator is determined by the charge and discharge rate of C412. The vertical hold control (R404) determines the resistance through which C412 charges and discharges, thus controlling the rate of charge and discharge.

The instantaneous conditions that exist in the circuit are illustrated by Fig. 34. Curves "A" and "B" show the voltages at the plates of the triodes while curve "C" shows the output waveform across R418 and C414. The action of R418 and C414 will be described in the next section, "VERTICAL DEFLECTION".

Fig. 34-D shows the voltage appearing at the grid of triode "B" and illustrates the method of synchronizing the multivibrator with the sync pulse. The solid line shows the

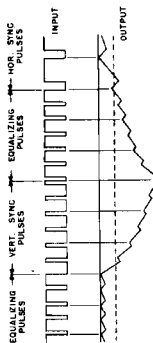


FIG. 32 - INTEGRATING ACTION

These conditions change, however, during the time of the vertical sync pulses. As shown, the duration of each vertical sync pulse is long as compared to the time between pulses. This allows C423 to charge to a relatively high value during the first pulse, and this charge cannot leak off completely in the short time between pulses. Then when the second pulse comes along, the charge it supplies will add to the charge remaining on the capacitor to increase the total charge. Thus the charge will increase with each successive pulse.

During the time of the equalizing pulses following the vertical sync pulses, the pulse duration is again short as compared to the time between pulses. The result is that the charge on C423 decreases in steps.

By virtue of the low-pass filter characteristics of the network, noise and any other high-frequency components are greatly attenuated, and the output of the network consists essentially of a single pulse correctly shaped to trigger the vertical multivibrator.

MULTIVIBRATOR ACTION (Fig. 33)

The "one shot" type of multivibrator is used in the vertical and horizontal deflection circuits of the V-2150-01 chassis. The operation of the multivibrator as a "free running" oscillator will first be described; then the action of the sync pulses will be introduced.

Referring to Fig. 33, which is a simplified diagram of the vertical multivibrator, assume that the plate voltage has not yet been applied; under this condition, there will be no charge across C412. When plate voltage is applied, C412 will charge and the polarity will be as indicated on Fig. 33. The heavy current flow required to charge C412 will follow the path indicated by heavy arrows. This current flowing through R414 develops a high bias voltage on the cathode of triode "A", and the same current flowing through R428 causes a drop in the plate voltage applied to triode "A". Under these conditions, triode "A" is inoperative because it is held below cut-off. Triode "B" will conduct during this period.

across C414 and the voltage across C414 is then at a low value. However, when the tube is driven into cut-off, the full B plus voltage is applied to C414. The capacitor will then charge along an exponential curve until the tube is driven into conduction. As soon as the tube conducts, C414 discharges rapidly. The process continues, and the slow charge and rapid discharge of C414 develops a saw-tooth voltage.

As shown in Fig. 34-C, the output of the multivibrator consists of a sharp spike of negative voltage in addition to the saw-tooth developed by C414. This spike of voltage is developed across the waveform correction resistor, R418, during the rapid discharge period. The resultant output waveform (Fig. 34-C), is required in order to obtain a saw-tooth of current in the deflection coil. This is so because all practical coils contain some resistance in addition to inductance. A saw-tooth of voltage will produce a saw-tooth of current through a resistor, but a square-wave of voltage is required to produce a saw-tooth of current through a pure inductance. Therefore, a composite saw-tooth and square wave is required to produce a saw-tooth of current in a practical coil.

The height control, R401, determines the amount of B plus voltage applied to the charging capacitor, C414. In so doing, it determines the amplitude of the voltage to which C414 charges and, as a result, the amplitude of the vertical multivibrator output.

The output of the multivibrator is coupled to the vertical output tube through C419. From here, the voltage is amplified and applied to the deflection coil through the vertical output transformer. The output transformer serves as an impedance matching device which matches the impedance of the deflection coil to the plate impedance of the output tube.

Since the saw-tooth voltage has some curvature due to the exponential charging rate of C414, some means of correction must be used to make the sweep linear. This is accomplished by using the vertical linearity control, R402, to adjust the bias on the vertical output tube. At some point on the characteristic curve of the tube, the curvature will be such that the undesirable curve on the applied saw-tooth will be cancelled by the tube amplification curve, and the output from the tube will be in the shape of a linear saw-tooth.

Damping resistors (R444 and R445) are placed across the deflection coils to prevent the development of transients that could cause picture distortion if they were not suppressed. These transients could be developed in the deflection coils as a result of the high harmonic content of the saw-tooth voltage; one of the harmonics could shock excite the coils into oscillation at their self-resonant frequency.

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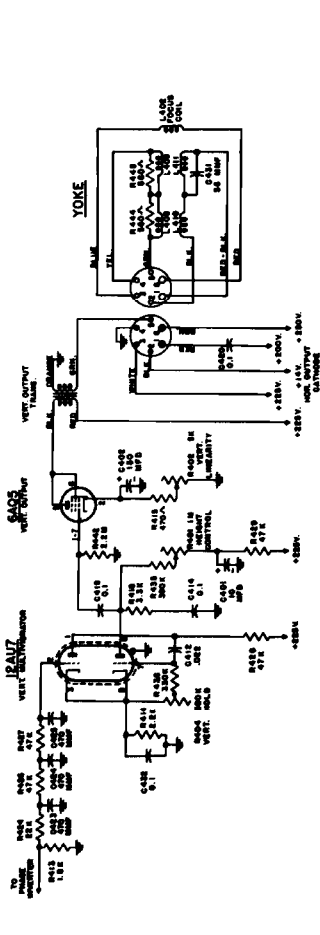


FIG. 35 - VERTICAL DEFLECTION CIRCUIT and the negative half of the sawtooth, along with the combined resultant, when the circuits are synchronized, when the sawtooth lags the sync pulse, and when the sawtooth leads the sync pulse.

The purpose of the horizontal AFC circuit is to correct for any fluctuations in horizontal multivibrator frequency that may be caused by noise, etc. This is accomplished by comparing the phase of the sawtooth output from the horizontal deflection circuits with that of the transmitted sync pulses and deriving a correction voltage that is applied to the horizontal multivibrator.

A portion of the horizontal deflection voltage is fed through C420, R439, and C426 to cathode #5 and plate #7 of the 6AL5 tube. This voltage, which is a square wave at the plates of the 7A5 tubes, becomes a sawtooth after passing through C420, R439, and C426. To the cathode #1 of the tube is applied a negative sync pulse from the phase inverter, and to the plate #2 is applied a positive sync pulse that occurs at exactly the same time as the negative pulse applied to cathode #1.

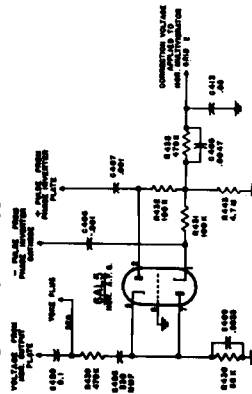


FIG. 36 - HORIZONTAL AFC

Figure 37 illustrates how the correction voltage is obtained as a result of the applied comparison voltages. The applied waveforms, instantaneous potentials developed as a result of the waveforms, and the directions of current flow during the positive half and the negative half of the sawtooth are shown on the simplified schematics. Also illustrated are the diode currents during the positive half

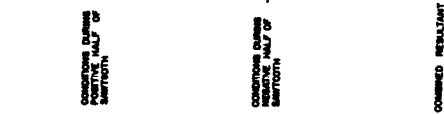


FIG. 37 - HORIZONTAL AFC ACTION

synchronized, and the combined resultant as shown in waveform #6 will be predominantly negative. The average of the voltage across R443 will then be negative in polarity. This voltage is applied to the grid of the multivibrator as a correction voltage to increase the multivibrator frequency. R438, C408 and C413 (Fig. 36) form a delay network which averages out the D-C fluctuations and tends to make the system immune to noise pulses.

The reverse of the foregoing conditions result when the sawtooth leads the sync pulse. As shown in waveforms #7, #8, and #9, the average output is positive in polarity. The positive voltage obtained thusly is applied to the multivibrator grid in order to decrease the frequency of the multivibrator and restore synchronism.

HORIZONTAL DEFLECTION CIRCUITS (Fig. 38)

Although the horizontal multivibrator differs somewhat from the previously-described vertical multivibrator, the operation is basically the same. One noteworthy difference is that while the frequency of the vertical multivibrator is controlled by direct application of the vertical sync pulses, the frequency of the horizontal multivibrator is controlled by a bias voltage which is developed by the AFC circuit and applied to grid #2 of the multivibrator. In addition, the horizontal multivibrator has a resonant circuit (ringing coil, L403) in one of its plate circuits in order that the output characteristics will include those of the sine wave oscillator as well as those of the multivibrator.

As shown in Fig. 39, the voltage waveform at plate #1 is essentially a sine wave with a superimposed impulse component. The same waveform would appear across the multivibrator output if C427 were not included in the circuit. However, the charge and discharge of

C427, as controlled by the multivibrator, produces a sawtooth waveform. This action is described in greater detail under "VERTICAL DEFLECTION CIRCUIT". The impulse component on the applied waveform provides the rapid return-time that is required.

If the frequency of the multivibrator increases in relation to the sync pulse frequency, the AFC circuit will develop a positive output voltage as described in the preceding section. This positive voltage, when applied to the grid of the multivibrator, will increase the plate current of the triode section during its conduction period. This, in turn, will increase the voltage drop across R420, thus decreasing the voltage at the plate (pin #1) during the conduction period. During this period C428, which had previously been charged to the full plate voltage, will discharge through R433 and R403 until its charge equals the voltage on plate #1. Since C428 must discharge to a lower-than-normal level, its discharge time is longer than when the circuits are synchronized, and the multivibrator frequency will decrease until the circuits are again synchronized.

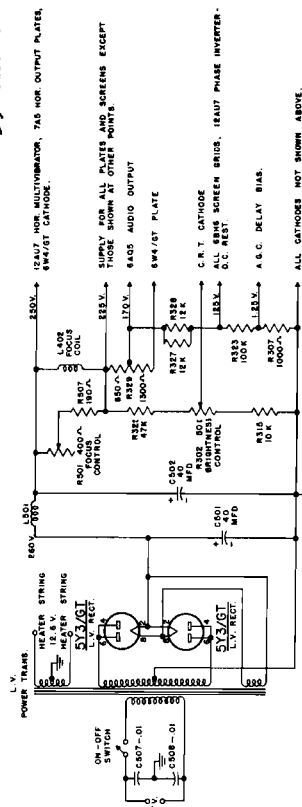


FIG. 41 - LOW VOLTAGE POWER SUPPLY

Filament voltage for the 1B3/GT rectifier is obtained through a separate winding on the high voltage R-F coil.

A feature of this type power supply is that personal contact with the high voltage lead will result in de-tuning of the circuit, and maximum voltage will not be produced under this condition. Thus the possibility of receiving a lethal shock is greatly reduced. However, extreme care should always be taken when working near high voltage circuits.

LOW VOLTAGE POWER SUPPLY (Fig. 41)

The low voltage power supply is a full-wave rectifier type. In order to handle the relatively large current requirement of the

FIG. 38 - HORIZONTAL DEFLECTION CIRCUITS

The horizontal output is coupled to the high impedance deflection coils, L410 and L411, through the coupling capacitor, C420. Plate voltage is applied to the output tubes through L401 which serves as a plate load impedance.

In addition to the sawtooth developed by the charge and discharge of C427, the output of the multivibrator contains a sharp spike of negative voltage. This voltage is developed across the waveform correction resistor, R422, during the rapid discharge period. An important function of this voltage is to drive the grids of the horizontal output tubes well below cut-off during the retrace time. A pulse of approximately 2500 volts is applied to the plates of the output tubes during the retrace time, and if the grids were not driven well

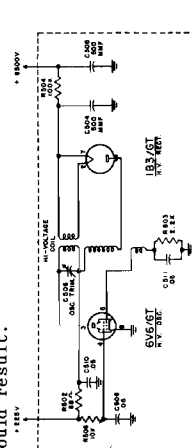


FIG. 40 - HIGH VOLTAGE POWER SUPPLY

The high voltage power supply utilizes a 6V6/GT tube operating as a tuned-plate, untuned-grid oscillator at approximately 285 kc. A high R-F voltage is developed across the resonant high-voltage coil. This R-F voltage is coupled to the plate of a 1B3/GT half-wave rectifier where it is converted to pulsating D-C. Due to the relatively high frequency, filtering is simplified, and an RC filter network composed of C504, R504, and C505 serves to remove the high frequency ripple component from the output. The filtered output of 8500 volts D-C is applied to the second anode of the cathode ray tube.

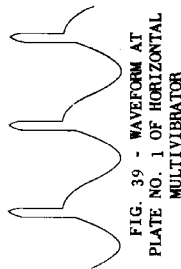


FIG. 39 - WAVEFORM AT PLATE NO. 1 OF HORIZONTAL MULTIVIBRATOR

below cut-off, the output tubes or associated components could be damaged by the heavy current that would flow.

The output from the multivibrator is applied to the grids of the horizontal output tubes through C411 and two parasitic suppressors, R411 and R412. The picture width is controlled in the horizontal output stage by varying the B plus applied to the plates of the tubes. The amplitude of the D-C plate voltage determines the amplitude of the sawtooth output to the deflection coils.

receiver, two 5Y3/GT tubes are employed and their plates are connected in parallel. The rectifier output, approximately 260 volts, is filtered by a single section pi-type network.

Practically all of the current drawn by the receiver flows through the parallel focus coil and focus control. The current flow through the focus coil is determined by the focus control which shunts some of the current around the coil.

The voltage is divided into various values by dividers and dropping resistors for application to the appropriate circuits in the receiver. These voltages are illustrated in Fig. 41.

ALIGNMENT

TEST EQUIPMENT - To properly service the V-2150-01 chassis, the following test equipment should be available:

1. R-F sweep generator which meets the following requirements:

- Frequency range from 18 to 30 mc. with a sweep width of 10 mc.
- Output adjustable with at least 100,000 microvolts maximum and a very low minimum.
- Output "flat" on all attenuator positions.

2. Cathode-ray oscilloscope, preferably one with a wide band vertical deflection amplifier and a low-capacitance input probe.

3. Signal generator capable of providing output frequencies listed below.

- 23.6 mc. 1st I-F.
- 24.1 mc. 2nd I-F.
- 26.1 mc. 3rd I-F.
- 25.5 mc. 4th I-F.
- 4.5 mc. Audio I-F and ratio detector (the frequency must be extremely accurate, preferably crystal controlled.)

NOTE: The R-F output level on all of the above frequencies should be adjustable with at least 100,000 microvolts maximum and a very low minimum.

4. Heterodyne frequency meter with crystal calibrator (if the signal generator does not include a crystal calibrator).
5. Electronic voltmeter (vacuum tube voltmeter), preferably one with a high voltage multiplier probe for measurements up to 10,000 volts.

GENERAL INFORMATION - All test equipment and the chassis should be bonded together by short lengths of heavy (1/8 inch) braided copper ribbon. The interconnecting leads should be shielded (72 ohm coaxial cable) and should be as short as possible consistent with ease of making connections. The effectiveness of the bonding can be checked during alignment by placing the hand on the metal chassis or test equipment case. If the response pattern or meter reading changes visibly, the bonding must be improved before the circuits are aligned.

COMMON I-F ALIGNMENT PROCEDURE

1. Rotate the channel selector switch to channel 3.
2. Connect the signal generator to the mixer tube through the coupling device shown in Fig. 42. The device is constructed by squeezing together a miniature tube shield until it fits the tube snugly and does not ground to the chassis. A .005 mfd capacitor is then soldered to the side of the shield. By sliding the tube shield up or down on the tube, the capacitance between the shield and the tube elements can be varied to obtain additional control of the coupling over that provided by the attenuator in the generator itself. The ground side of the generator output cable should be connected to the receiver chassis.
3. Connect a vacuum tube voltmeter to the video test jack on the receiver chassis, and set the meter to its 5 volt scale.

4. Set the signal generator to 23.6 mc. (unmodulated), and adjust L306 for maximum voltage on the VTVM. During this adjustment, keep the signal generator output adjusted so that the VTVM reading does not exceed 2 volts.

5. Set the signal generator to 24.1 mc. (unmodulated), and adjust L307 for maximum voltage on the VTVM. During this adjustment, keep the signal generator output adjusted so that the VTVM reading does not exceed 2 volts.

6. Set the signal generator to 26.1 mc. (unmodulated), and adjust L308 for maximum voltage on the VTVM. During this adjustment, keep the signal generator output adjusted so that the VTVM reading does not exceed 2 volts.

7. Set the signal generator to 25.5 mc. (unmodulated), and adjust L309 for maximum voltage on the VTVM. During this adjustment, keep the signal generator output adjusted so that the VTVM reading does not exceed 2 volts.

8. Connect the sweep generator to the mixer tube through the coupling device previously described. The signal generator will be used in the following steps to provide marker indications at various frequencies on the response curve. In this application, the signal generator input to the set must be low in amplitude to avoid distorting the response curve. To reduce the signal generator input accordingly, the signal generator should be loosely coupled to the set by wrapping a few turns of insulated wire around the coupling capacitor "pigtail" and connecting the signal generator to this wire.

9. Connect the vertical input of the oscilloscope to the video test jack through the de-coupling network shown in Fig. 43. The oscilloscope horizontal input should be connected to the sweep output from the sweep generator; turn the "X" or "OFF" position.

10. Adjust the sweep generator for a center frequency of 25.3 mc. with a 6 mc. deviation. Adjust the sweep generator output until a setting is found where the pattern on the oscilloscope will not change in amplitude nor shape when the contrast control is rotated through its full range.

The oscilloscope pattern obtained should be similar to that shown in Fig. 44. Use the signal generator as a marker to check at the frequencies indicated. If the pattern obtained is not similar to Fig. 44, L306, L307, L308, and L309 should be re-adjusted to produce the correct pattern. The effect of the adjustments will be as follows:

- L306 affects the low frequency side of the curve
- L307 affects the center portion of the curve
- L308 affects the high frequency side of the curve
- L309 affects the position of the video I-F carrier (26.1 mc.) on the curve

AUDIO I-F ALIGNMENT PROCEDURE

1. Connect the "high" side of the signal generator to the video test jack through a .001 mfd capacitor, and ground the "low" side to the chassis.
2. Connect the vacuum tube voltmeter to the points indicated on the bottom view of the chassis, Fig. 46. The common lead should connect to point "C", and the "high" lead should connect to point "A". Set the meter on its 5 volt (-DC) scale.

3. Adjust the signal generator to 4.5 mc. (unmodulated). The accuracy of this frequency is very important. If a crystal controlled signal generator is not available, the frequency should be checked using a frequency meter with a crystal calibrator.

4. Adjust L201 and L202 for maximum voltage on the VTVM. During this adjustment, keep the signal generator output adjusted so that the VTVM reading does not exceed 5 volts.

5. Connect the common lead from the VTVM to point "A" (Fig. 46), and connect the "high" lead to point "B". Here it is important that the case and components of the VTVM are not grounded to the receiver chassis; otherwise, point "A" would be shorted to the chassis through the common lead.

6. Using the same signal generator amplitude and frequency as in step 4, adjust L203 for zero voltage on the VTVM. As the adjustment is tuned through resonance, the voltage will rapidly change from one polarity to the opposite polarity; the point where the voltage is zero is the correct setting.

H-F OSCILLATOR ALIGNMENT PROCEDURE

If the 6C4 H-F oscillator tube is replaced, the different inter-electrode capacitance of the new tube may change the oscillator frequency enough to necessitate re-alignment of the oscillator.

The oscillator adjusting screws are located on the front of the tuner assembly, and the following procedure should be followed for their adjustment:

1. Remove the chassis from the cabinet and make the connections necessary to receive an on-the-air signal.
2. Set the fine tuning control to the middle of its range, and leave it in this position during the following adjustments.
3. Set the channel selector switch to the highest of the low band (channels 2 through 6) stations operating in your locality.
4. Peak the appropriate oscillator slug for the best picture detail.
5. Repeat step 4 for each progressively lower channel on which a station transmits in your area.
6. Set the channel selector switch to the highest of the high band (channels 7 through 13) stations operating in your locality.
7. Peak the appropriate oscillator slug for the best picture detail.
8. Repeat step 7 for each progressively lower channel in the high band on which a nearby station transmits.
9. Check the previously-made low band adjustments, and if the tuning has changed repeat steps 3 through 8.

MODEL H-223, ch. V-2150-01.

The foregoing written procedure should be studied before using this chart.

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FOCUS ADJ.

[illegible]

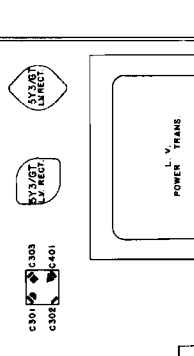
FIG. 45—TOP VIEW OF CHASSIS

68H6
AUDIO 1 F

5 L202 4 60

6AL5
RAYON

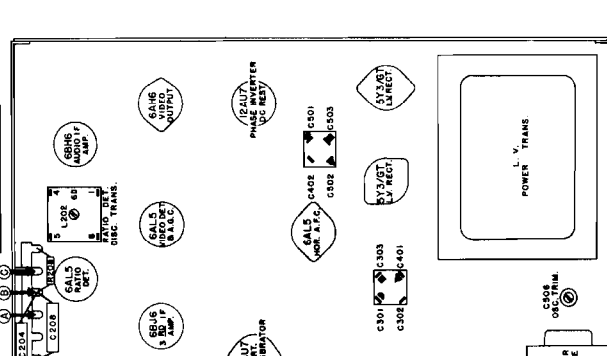
L208

[illegible]

CS06
OSC. TRIM.



ST



SSIS

MODEL H-223, Ch. V-2150-01

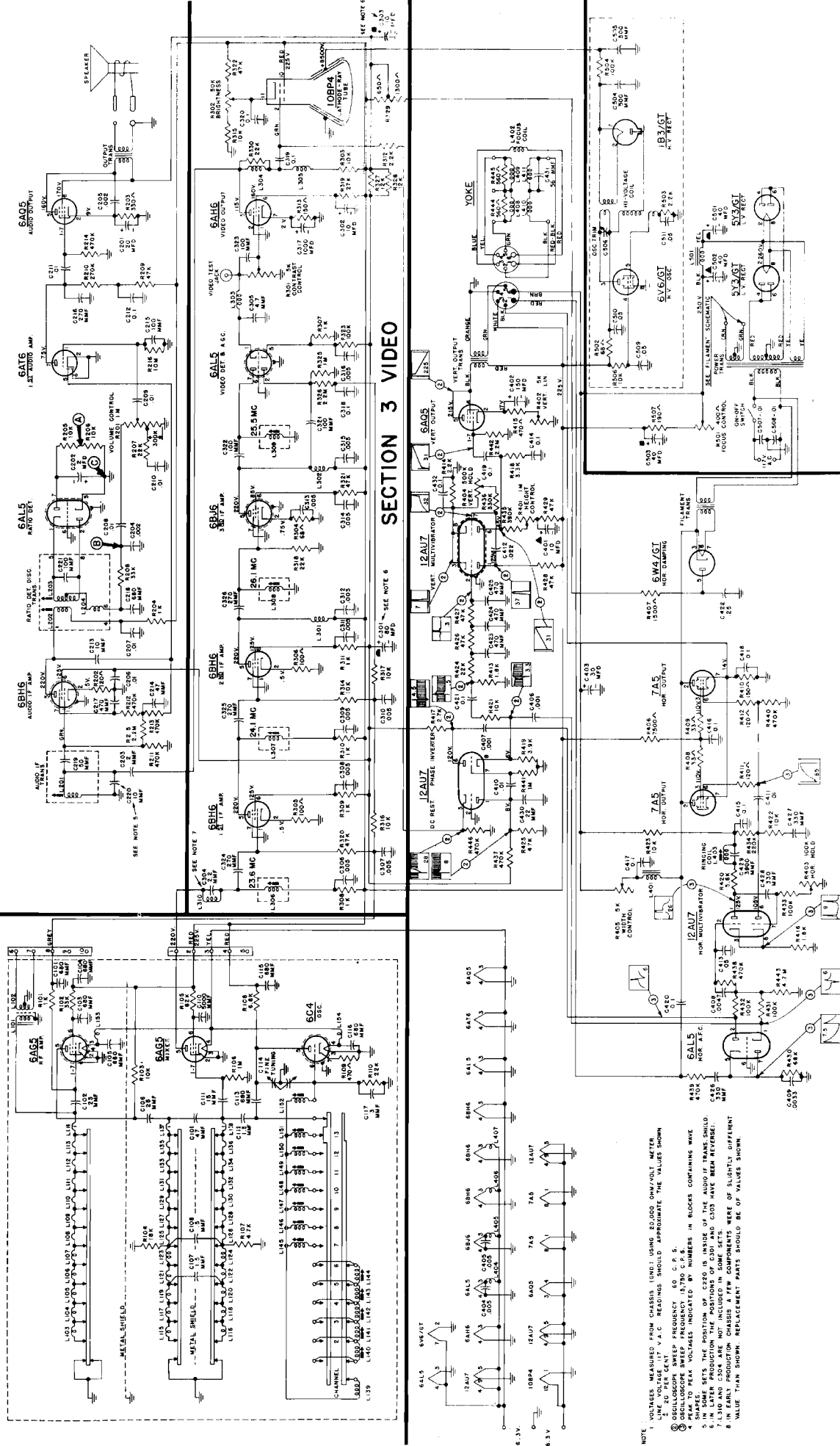
SECTION 1 RF

SECTION 2 AUDIO

SECTION 3 VIDEO

SECTION 4 SWEEP

SECTION 5 POWER



CHASSIS NO. V-2150-01

FIG. 47—SCHEMATIC DIAGRAM

IMPORTANT—Since many of the components are very critical, exact duplicates must be used for replacement purposes.

When ordering parts, specify model number of set in addition to part number and description

Section 2 - Values	
301	X4.9891
302	X4.9891
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304	X4.9891
305	X4.9891
306	X4.9891
307	X4.9891
308	X4.9891
309	X4.9891
310	X4.9891
311	X4.9891
312	X4.9891
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428	X4.9891
429	X4.9891
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431	X4.9891
432	X4.9891
433	X4.9891
434	X4.9891
435	X4.9891
436	X4.9891
437	X4.9891
438	X4.9891
439	X4.9891
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442	X4.9891
443	X4.9891
444	X4.9891
445	X4.9891

Ref.	Part No.	Description	Function
R-419	RC20AE1623K	Resistor, 1600 ohms $\frac{1}{2}$ w.	Grid-to-Base
R-420	RC20AE1623K	Resistor, 1600 ohms $\frac{1}{2}$ w.	Plate Load
R-421	RC20AE103K	Resistor, 10,000 ohms $\frac{1}{2}$ w.	Decoupling
R-422	RC20AE103K	Resistor, 10,000 ohms $\frac{1}{2}$ w.	Decoupling
R-423	RC20AE103K	Resistor, 10,000 ohms $\frac{1}{2}$ w.	Re-form Correction
R-424	RC20AE223K	Resistor, 22,000 ohms $\frac{1}{2}$ w.	Re-form Correction
R-425	RC20AE223K	Resistor, 22,000 ohms $\frac{1}{2}$ w.	Re-form Correction
R-426	RC20AE223K	Resistor, 22,000 ohms $\frac{1}{2}$ w.	Re-form Correction
R-427	RC20AE473K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-428	RC20AE473K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-429	RC20AE473K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-430	RC20AE473K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-431	RC20AE473K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-432	RC20AE473K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-433	RC20AE104I	Resistor, 100,000 ohms $\frac{1}{2}$ w.	AFC Diode Load
R-434	RC20AE104I	Resistor, 100,000 ohms $\frac{1}{2}$ w.	AFC Diode Load
R-435	RC20AE104I	Resistor, 100,000 ohms $\frac{1}{2}$ w.	Multi-brancher Plate Load
R-436	RC20AE104I	Resistor, 100,000 ohms $\frac{1}{2}$ w.	Multi-brancher Plate Load
R-437	RC20AE104I	Resistor, 100,000 ohms $\frac{1}{2}$ w.	Multi-brancher Plate Load
R-438	RC20AE474K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-439	RC20AE474K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-440	RC20AE474K	Resistor, 47,000 ohms $\frac{1}{2}$ w.	Plate Dropping
R-441	RC20AE103K	Resistor, 10,000 ohms $\frac{1}{2}$ w.	Grid Return
R-442	RC20AE103K	Resistor, 10,000 ohms $\frac{1}{2}$ w.	Grid Return
R-443	RC20AE103K	Resistor, 10,000 ohms $\frac{1}{2}$ w.	Grid Return
R-444	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	D.C. Screen
R-445	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-446	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-447	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-448	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-449	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-450	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-451	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-452	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-453	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-454	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-455	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-456	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-457	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-458	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-459	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-460	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-461	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-462	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-463	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-464	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-465	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-466	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-467	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-468	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-469	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-470	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-471	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-472	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-473	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-474	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-475	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-476	Part V-6486-1	Resistor, 560 ohms $\frac{1}{2}$ w.	Transient Damping
R-477	Part V-6486-1	Resistor, 560 ohms <	